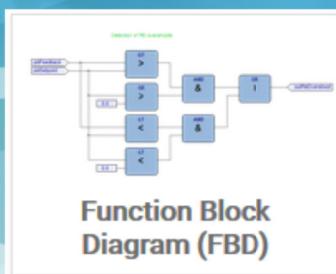


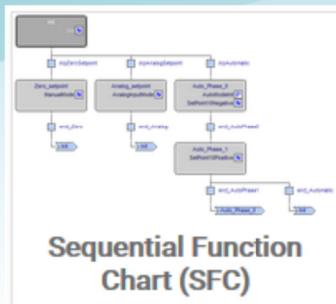
Ladder Diagram (LD)



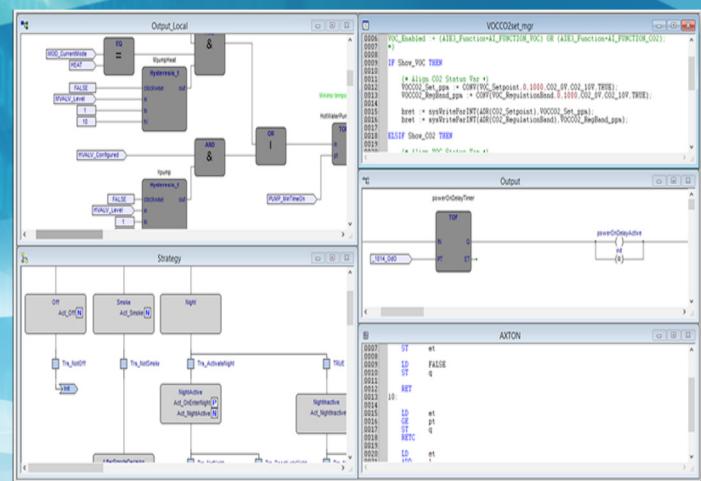
Structured Text (ST)



Function Block Diagram (FBD)



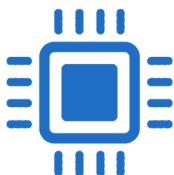
Sequential Function Chart (SFC)



Diagos

Softplc for Smitec systems

Application developer suite using programming language IEC 61131-3 for Smitec automation systems. Flexibility, performance and acquired know-how will allow for developing and managing applications most effectively.



X86 & ARM



Linux



Realtime



Knowhow



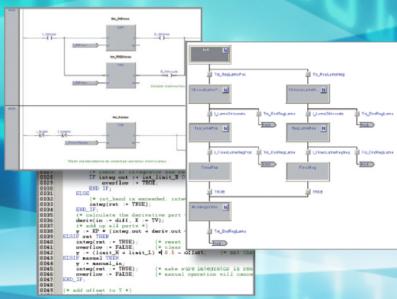
Smitec S.p.A.

Smitec S.p.A.
Via V. Veneto, 4 - 24016 San Pellegrino Terme (BG) ITALY
Tel. +39 0345 40.800 - Fax: +39 0345 40.809 - www.smitec.it

Head office:

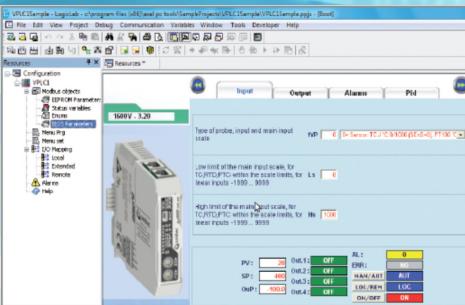
Via C. Ceresa, 10 - 24015 San Giovanni Bianco (BG) ITALY
C.F. e P. IVA IT03790400166

Funzionalità



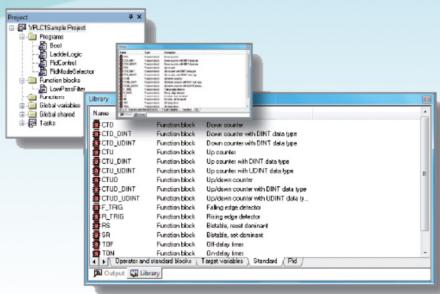
IEC 61131-3

- Instruction List (IL)
- Structured Text (ST)
- Ladder Diagram (LD)
- Function Block Diagram (FBD)
- Sequential Function Chart (SFC)



PROJECT

- Graphic system design
- Employment of predefined devices (I/O Modules and Drives)
- Availability of functional and motion libraries
- Integrable PLC Open libraries
- Use of acquired know-how



PROJECT SOURCES

- Sources on development system
- If necessary encrypted sources on the device
- Sources on the device can be decrypted only by enabled users
- Encrypting of parts of sources
- Availability of encrypted libraries for distribution

DEBUG

- Animation of LD and SFC diagrams
- Real-time monitoring of system variables
- Oscilloscope
- Breakpoint and Trigger
- HotSwap (code replacement without system power off)

SIMULATION

- Simulation on PC of the system
- Graphic rendering of I/O status
- Variables and input status setting
- Application debug without system connected
- Application debug

